

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re reissue patent application of

Keiichiro KATA, et al.

Serial No.: 10/645,782



Application for reissue of U.S. Patent No. 5,844,304, granted Dec. 1, 1998

Filed: August 22, 2003

For: PROCESS FOR MANUFACTURING SEMICONDUCTOR DEVICE AND SEMICONDUCTOR WAFER

SUPPLEMENTAL REISSUE DECLARATION

Box REISSUE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

We, Keiichiro Kata and Shinichi Chikaki, declare that:

1. The entire contents of the Reissue Declaration filed on August 22, 2003, are incorporated in its entirety herein by reference.
2. We believe that every error in the original patent which was corrected in the present reissue application, and is not covered by the prior oath/declaration submitted in this application, arose without any deceptive intention on the part of the Applicant.
3. We hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

**Keiichiro Kata**

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**Shinichi Chikaki**

Signature: Shinichi Chikaki Date: July 3, 2006

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